

## About the College

PSG College of Technology, started in the year 1951, is an autonomous college for the past 33 years, offering 22 undergraduate programmes and 37 postgraduate programmes. It also offers research programmes leading to MS/Ph.D. degrees in all departments. Currently more than 550 research scholars, both full time and part time, are working for their Ph.D. degree.

PSG College of Technology, an AICTE approved institution is affiliated to Anna University, and is ISO 9001 certified. PSG College of Technology believes in creating and disseminating knowledge and skills in core and frontier disciplines through innovative educational programmes, industrial training, research & consultancy and developing a new cadre of professionals with a high level of competence, deep sense of social commitment and ethics. PSG College of Technology has collaborative arrangements with industries, research laboratories and Universities abroad in various areas of joint academic programmes, research and training.

### CHAIRMAN

**Dr. R. RUDRAMOORTHY**

Principal, PSG College of Technology

### CONVENORS

**Dr. M. C. Bhuvaneshwari**

HoD -I/c EEE

### ORGANIZING SECRETARIES

**Ms. A. Gunasundari, Assistant Professor, EEE**

**Dr. D. Janaki Sathya, Assistant Professor, EEE**

### Important Dates to Remember:

Last date for receipt of application : 20/05/2018

Acceptance will be intimated to participants on : 30/05/2018

*The details of this workshop are available in the website [www.psgtech.edu](http://www.psgtech.edu)*

## Resource Persons

Mr. R. RAJESH is a Embedded software Engineer at Oppila Microsystems Pvt, Ltd, Bangalore. He has done. B.Tech(ECE). He has 3 years of Experience in software development, and contributing various projects in DRDO, Bangalore and IGCAR, Kalpakkam. Working in Anadigm's Development and technical support more than 2 years and have given lectures in various universities about FPAA in India.

Mr. S. NATESAN is a Design Engineer at Oppila Microsystems Pvt, Ltd, Bangalore. He has done. B. E (ECE) and has 2 years of Experience in hardware design, and contributing various projects in ADE, Bangalore and NAL, Bangalore.

## About Oppila Microsystems

Oppila, a private limited company headquartered in Bangalore is established by a group of experienced professionals having 100 man years of experience in the field of embedded systems and hardware design. Oppila is into embedded systems, hardware design, product development and exclusive sales & technical support provider for Anadigm, Inc., USA, manufacturer of FPAA.

## TOPICS TO BE COVERED

FPAA

EDA Tool & simulation

FPAA architecture and CAM

Evaluation board

Static configuration

Dynamic reconfiguration

## PSG COLLEGE OF TECHNOLOGY COIMBATORE

### NATIONAL HANDS ON WORKSHOP ON FIELD PROGRAMMABLE ANALOG ARRAY (FPAA) FOR ANALOG SYSTEM DESIGN

**22 - 23 June 2018**

**Convenor  
Dr. M.C. Bhuvaneshwari**

**Organizing Secretary  
Ms. A. Gunasundari  
Dr. D. Janaki Sathya**

Organised by



**Dept. of Electrical & Electronics Engineering  
PSG College of Technology  
Peelamedu, Coimbatore - 641 004  
Tamil Nadu  
Phone: 0422-2572177, 2572477  
Fax: 0422-2573833**

## About the Seminar

The main aim and motivation of organizing this workshop is to explore the students, academicians and industry personnel to the design of adaptive analog systems, using FPAA in the area of control and measurement systems, analog signal processing and rapid prototyping.

### Workshop Schedule

#### Day 1: June 22, 2018

##### Registration

9:00 am-9:15 am

##### Inauguration

9:15 am - 9:30 am

##### Introduction to FPAA Anadigm Designer 2 - EDA Tool

9:30 am - 11:00 am

##### Tea Break

11:00 am - 11:15 am

##### Simulation and FPAA Configuration - Hands On

11:15 am - 12:30 pm

##### Lunch

12:30 pm - 1:30 pm

##### IO Pin Configuration

1:30 pm - 2:15 pm

##### Static Configuration - Hands On

2:15 pm - 2:45 pm

##### Tea Break

2:45 pm - 3:00 pm

##### Audio Signal Processing in FPAA

##### (Design and Implementation)

3:00 pm - 5.00 pm

#### Day 2: June 23, 2018

##### Dynamic Reconfiguration - Various Method

##### (Algorithmic, State- Driven, PC- Driven)

9:00 am - 10:00 am

##### Tea Break

10:15 am - 10:30 am

##### Dynamic Reconfiguration Using Windows Application - Hands On

10:30 am - 11:00 am

##### Dynamic Reconfiguration - Audio Signal Processing in FPAA - Hands On

11:30 am - 12:30 pm

##### Lunch

12:30 pm - 1:30 pm

##### Filter Design and Implementation - Hands On

1:30 pm - 2:30 pm

##### Tea Break

2:30 pm - 2:45 pm

##### FPAA in Various Applications - DEMO

2:45 pm - 4:30 pm

##### Valedictory and Certificate Distribution

4:30 pm - 5:00 pm

## About the EEE Department

The Department of Electrical and Electronics Engineering (EEE) has been playing a vital role in producing scientists and technologists of highest caliber ever since it was established in the year 1951. The department offers UG (Regular and Sandwich) programmes, PG programmes (Applied Electronics, Power Electronics & Drives, and Embedded & Real-Time Systems), and PhD programmes. The department along with its highly qualified faculty members started functioning right from inception and engages actively in teaching and research in all current areas of Electrical and Electronics Engineering. State of the art computational and experimental facilities enable the department to undertake basic and applied research and provide support to R&D organizations. The department has Centres of Excellence namely, PSG-LAPP Centre for Excellence in Cable Technology, PSG - L&T Centre for Excellence in LV Switchgear, Audio-Processing Centre and Centre for Renewable Energy.

### Eligibility

- Faculty working in Engineering / Polytechnic Colleges
- Engineers from R&D Organizations / Industries
- UG/PG Engineering Students
- Research Scholars

### Registration Fee

Students/ Research Scholar (FT)	Rs 1500/-
Faculty / Research Scholar (PT)	Rs 2000/-
Industry Participants	Rs 2500/-

*\*The registration fee includes service tax also.*

*\*The registration fee includes cost towards course material, lunch and refreshments.*

*\*Accommodation will not be provided for Faculty & Industry participants. For Students accommodation will be provided at dormitory on chargeable basis.*

### How to Apply?

Registration for workshop can be made by sending the duly filled application form along with Demand Draft drawn in favor of "PSG CNCE" payable at Coimbatore.

The applicants should send their applications in the specified format through their Principal or Sponsor to reach us on or before: **05/06/2018**.

**PSG COLLEGE OF TECHNOLOGY,  
COIMBATORE - 641 004**

**Dept. of Electrical & Electronics  
Engineering**

**NATIONAL HANDS ON WORKSHOP  
ON  
FIELD PROGRAMMABLE ANALOG ARAY  
FOR ANALOG SYSTEM DESIGN**

**22 - 23 June 2018**

**APPLICATION FORM**

Name (in Block Letters): .....

Date of birth & Age : .....

Qualification : .....

Experience (if any) : .....

Designation & Dept. : .....

Organization : .....

Address for

Communication : .....

Office No. : ..... Mob No. : .....

Email Id : .....

Demand Draft No. & Bank : .....

.....

Amount in Rs. : .....

### DECLARATION BY THE CANDIDATE

The given information is true to the best of my knowledge. I agree to abide by the rules and regulations governing the programme. If selected, I shall attend the course for the entire duration.

Place :

Date : Signature of the candidate

### Sponsorship

Mr./Ms./Dr. \_\_\_\_\_  
is an employee of our Institution / Industry and is hereby sponsored. He/she will be permitted to attend the workshop, if selected.

Office Seal

Place  
Date

Signature of the  
Sponsoring Authority