

ABOUT THE PROGRAMME

VLSI systems span a broad range of disciplines, including semiconductor devices and processing, integrated electronic circuits, digital logic, design disciplines and tools for creating complex systems and applications of complete VLSI systems. As the complexity of the electronic circuits increases, the need to use computers for their design becomes indispensable. Modern VLSI Computer Aided Design (CAD) systems allow the chip designer to access in a consistent and convenient way a variety of synthesis and analysis tools. Such tools have advanced considerably in the past several years, both in their scope and in their ability to handle large designs. The main objective of this STTP is to present the participants a broad foundation about CAD for Analog, Digital and Mixed IC Design Flow using Cadence EDA Tools. To improve the effectiveness of the system, the participants will be exposed to Data Structures, Optimization algorithms and Machine Learning techniques used in CAD Tools. In addition, the participants will get insight into VLSI Packaging, Hardware Security and Trust.

COURSE CONTENTS

- ❖ Full Custom, Semi Custom Design
- ❖ Cadence Tool Flow
- ❖ Mixed Signal IC Design
- ❖ Data Structures for CAD
- ❖ Optimization Algorithms for CAD
- ❖ Machine Learning in VLSI CAD
- ❖ VLSI Signal Processing
- ❖ Research issues in FPGA algorithms
- ❖ Hardware Security and Trust

RESOURCE PERSONS

Experts from industries and experienced faculty from renowned institutions will deliver lectures and handle practical sessions.

ORGANIZING COMMITTEE

CHIEF PATRON

Shri. L. Gopalakrishnan
Managing Trustee

PATRON

Dr. K. Prakasan
Principal Incharge

CONVENERS

Dr. K. Umamaheswari
Professor and Head, IT
Dr. S. Subha Rani
Professor and Head, ECE

COORDINATOR

Dr.K.Anitha kumari
Associate Professor, IT

CO-COORDINATORS

Dr. S. Hema Chitra
Assistant Professor (Sl. Gr), ECE
Dr. R. Rekha
Assistant Professor (Sl. Gr.), IT

ADDRESS FOR COMMUNICATION

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Associate Professor

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AICTE Sponsored

One Week

SHORT TERM
TRAINING PROGRAMME

on

COMPUTER AIDED DESIGN
FOR VLSI

11th – 16th November 2019



Organized by

Department of Information Technology
Department of Electronics and
Communication Engineering

PSG COLLEGE OF TECHNOLOGY

Peelamedu, Coimbatore - 641 004.

Tamil Nadu

ABOUT THE COLLEGE

PSG College of Technology, a premier institution for engineering, started in 1951, is well equipped with modern facilities of world class standards. The college is government-aided, autonomous, NAAC accredited, ISO 9001 certified and affiliated to Anna University. The college offers a total of 63 full time and part time programmes in Engineering, Technology, Science and Management at UG,PG and Doctoral levels. The institution holds extremely proud alumni at renowned institutions. The college maintains close interaction with several R&D institutions in India and abroad. It also has close collaborative links with industries. Several advanced centres are set up with financial support from the Ministry of Human Resources Development, DST and other agencies.

ABOUT THE DEPARTMENTS

The **Department of Information Technology** was established in the year 1999 with a vision to develop quality engineers to meet the current needs in the emerging world of IT. The department endeavors to educate students to acquire technical expertise and knowledge in the field of Information Technology. The department offers one undergraduate programme and two postgraduate programmes. The department undertakes projects sponsored by **AICTE, DST, UGC** with notable publications.

The **Department of Electronics and Communication Engineering** started in the year 1968, is celebrating its golden jubilee this academic year. Ever since its inception, it has been splendid in providing dynamic and quality engineers to the society till date. The Department has one Undergraduate programme and four Postgraduate Programmes. The department undertakes projects sponsored by **MeitY, DST, DRDO, UGC, AICTE**, etc.

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One Week Short Term Training Programme

on

Computer Aided design for VLSI

11th - 16th NOVEMBER 2019

Organized by

Department of IT, Department of ECE

PSG College of Technology, Coimbatore - 641 004.

REGISTRATION FORM

Name Dr./Mr./Mrs. :
(in Block letters)

Designation :

Department :

Name of the :

Institution

Address :

Mobile :

Email id :

Is the Institution approved by AICTE?(Y/N)

Accommodation required? (Y/N)

DD Number and Date :

Bank and Branch :

Declaration by Applicant

The given information is true to the best of my knowledge. I agree to abide by the rules and regulations governing the programme. If selected, I shall attend the workshop for the entire duration.

Place:

Date :

Signature of the Applicant

SPONSORSHIP

This is to certify that _____ is a regular employee of our institution and is hereby sponsored for AICTE sponsored one week STTP on "**Computer Aided Design for VLSI**" at PSG College of Technology, Coimbatore - 641 004 from **11.11.2019 to 16.11.2019**. He/She will be permitted to attend the course for the entire duration if selected.

Place :

Date :

Signature of the Principal with Seal

ELIGIBILITY

This course is open to Faculty members and Research scholars of **AICTE approved** Engineering colleges and Polytechnic colleges.

REGISTRATION DETAILS

The registration is free for faculty members from AICTE approved institutions. However, the participants are requested to make **payment of Rs.1000/- (upon acceptance of registration)** by demand draft drawn in favour of "**PSG CNCE**" payable at **Coimbatore**, refunded on completion of the course successfully. The number of participants is limited to **40** and the selection is based on first come – first served basis

ACCOMMODATION DETAILS

Accommodation will be provided to the outstation participants subject to the availability.

IMPORTANT DATES:

Last Date for Registration: **10.10.2019**

Intimation of Acceptance : **21.10.2019**